


REMARKS

Applicants have amended certain claims to improve their form. These amendments are not made for reasons related to patentability.

Prompt examination and favorable office action are respectfully requested.

Respectfully submitted,

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Version marked to show changes made

Claims 46, 47, 50 and 88-101 have been amended as follows:

46. (Amended) The method of claim 37, further including independently controlling the scrolling of plural background screens [display windows].

47. (Amended) The method of claim 37, further including independently controlling the rotation of plural background screens [display windows].

50. (Amended) The method of claim 37, further including independently controlling the upper-left and lower-right display coordinates [portions] of plural display windows.

88. (Amended) The cartridge [system] of claim 87, wherein said at least one [memory cartridge] non-volatile memory device stores instructions controlling storage, within said video random access memory storage, of bitmapped color information for the simultaneous display of up to 32,768 different colors.

89. (Amended) The cartridge [system] of claim 87, wherein said at least one [memory cartridge] non-volatile memory stores instructions that control reference to the color palette random access memory as a color lookup table for bitmapped graphics stored in said video random access memory storage to provide simultaneous display of up to 256 different colors.

90. (Amended) The cartridge [system] of claim 87, wherein said at least one [memory cartridge] non-volatile memory stores instructions that address the video random access memory storage beginning at address 06000000h.

91. (Amended) The cartridge [system] of claim 87, wherein said at least one [memory cartridge] non-volatile memory stores instructions that access two allocated [two] frame buffers in the video random access memory storage to provide full motion video.

92. (Amended) The cartridge [system] of claim 87, wherein said at least one [memory cartridge] non-volatile memory stores instructions that specify up to 128 different moving object definitions providing moving object characters of up to 12 different sizes.

93. (Amended) The cartridge [system] of claim 87, wherein said at least one [memory cartridge] non-volatile memory stores instructions that write rotation/scaling parameters to the object attribute memory storage.

94. (Amended) The cartridge [system] of claim 87, wherein said at least one [the memory cartridge] non-volatile memory stores instructions that write mosaic information to the object attribute memory storage.

95. (Amended) The cartridge [system] of claim 87, wherein said at least one [the memory cartridge] non-volatile memory stores instructions that control display of plural display windows simultaneously.

96. (Amended) The cartridge [system] of claim 87, wherein said at least one [the memory cartridge] non-volatile memory stores instructions that control the scrolling of plural background screens [display windows] independently.

97. (Amended) The cartridge [system] of claim 87, wherein said at least one [the memory cartridge] non-volatile memory stores instructions that control the rotation of plural background screens [display windows] independently.

98. (Amended) The cartridge [system] of claim 87, wherein said at least one [the memory cartridge] non-volatile memory stores instructions that control the alpha blending of plural display windows independently.

99. (Amended) The cartridge [system] of claim 87, wherein said at least one [the memory cartridge] non-volatile memory stores instructions that control the fade-in/fade-out of plural display windows independently.

100. (Amended) The cartridge [system] of claim 87, wherein said at least one [the memory cartridge] non-volatile memory stores instructions that control the upper-left and lower-right display coordinates [portions] of plural display windows independently.

101. (Amended) The cartridge [system] of claim 87, wherein said at least one [the memory cartridge] non-volatile memory stores instructions that control performance of arithmetic operations on two selected surfaces and processing for up to 16 levels of semi-transparency.